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functional block diagram

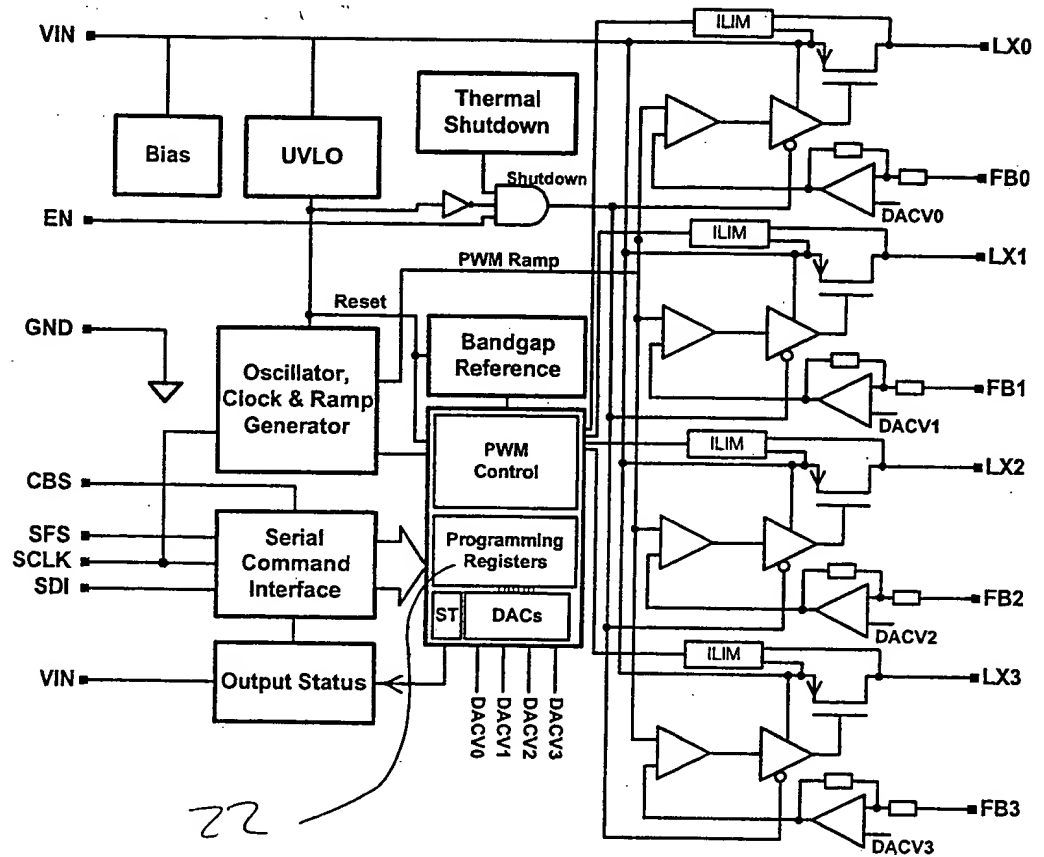


FIGURE 1

0994099-032101

serial control interface timing diagram

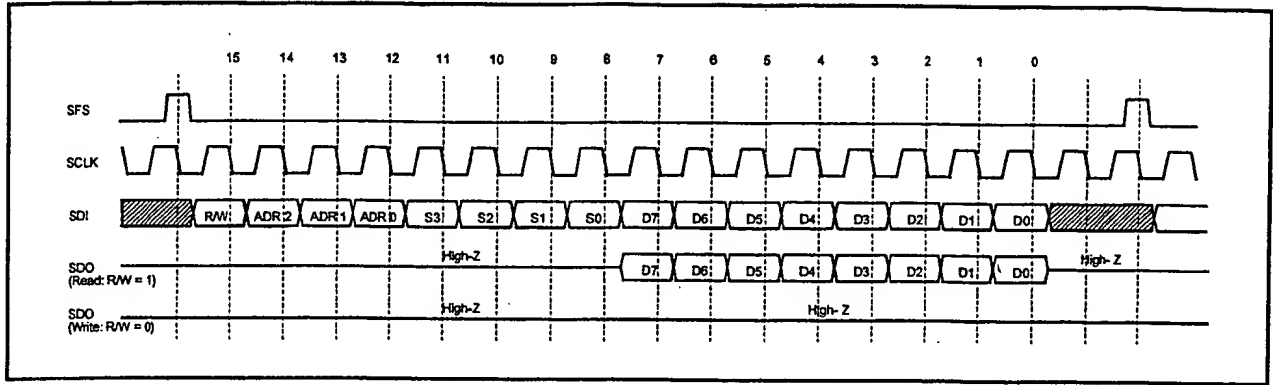


FIGURE 3

eight channel application circuit schematic

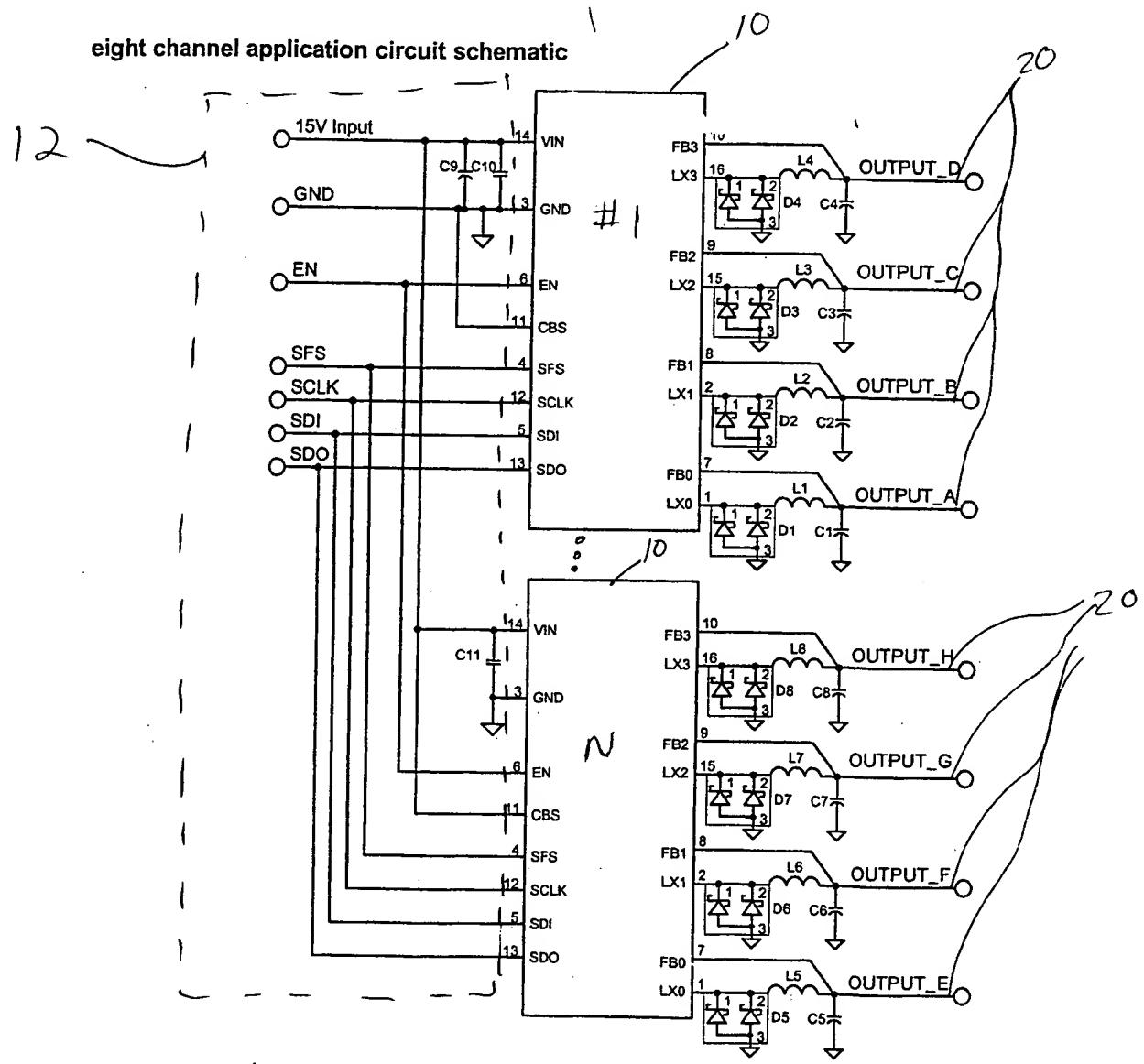


FIGURE 2

095406604E660

SCLK

Initialization
word

Device 1
÷8 Counter
STATE

Device 2
÷8 Counter
STATE

Device 1
÷8 Output

Device 2
÷8 Output

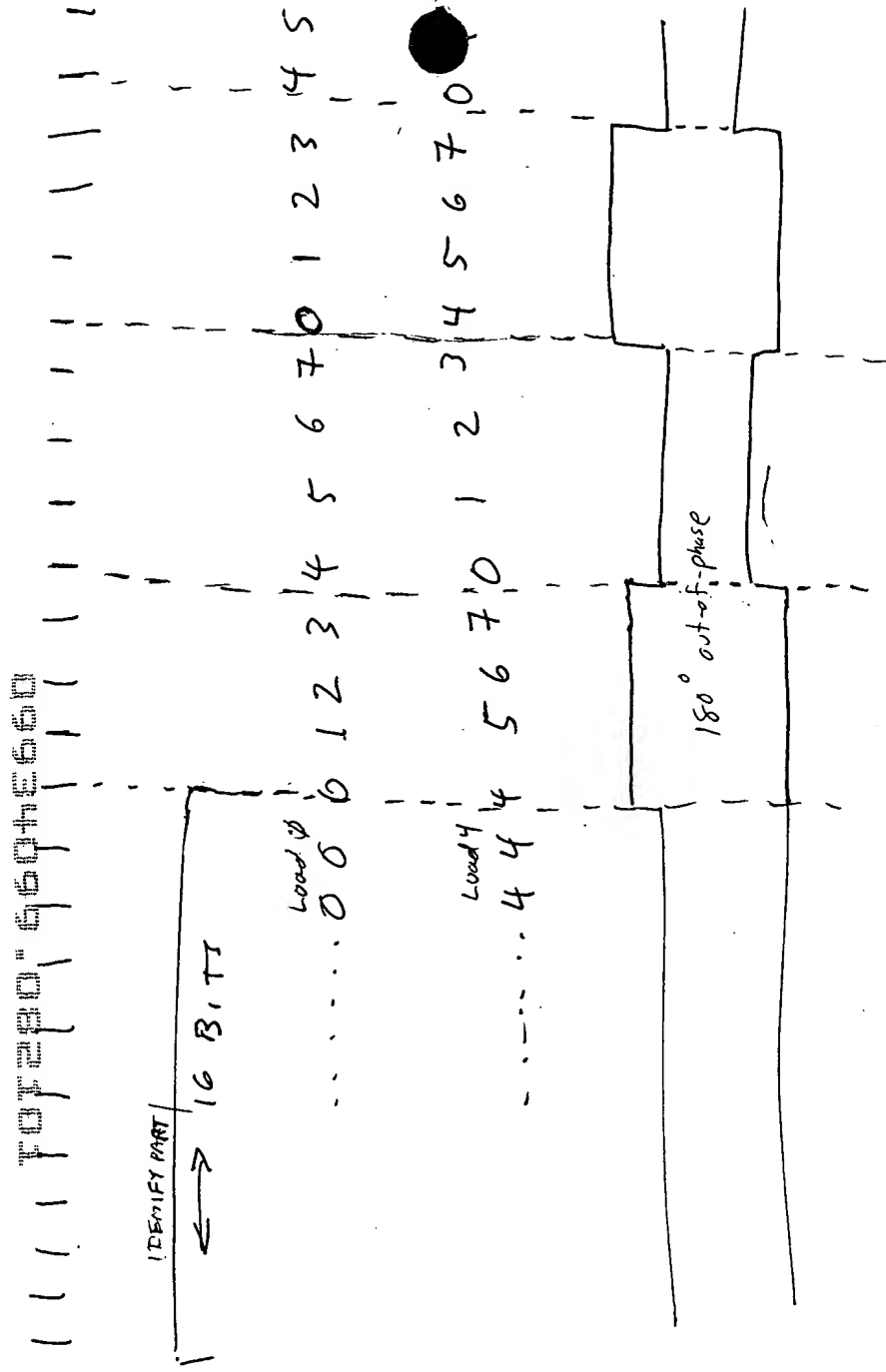
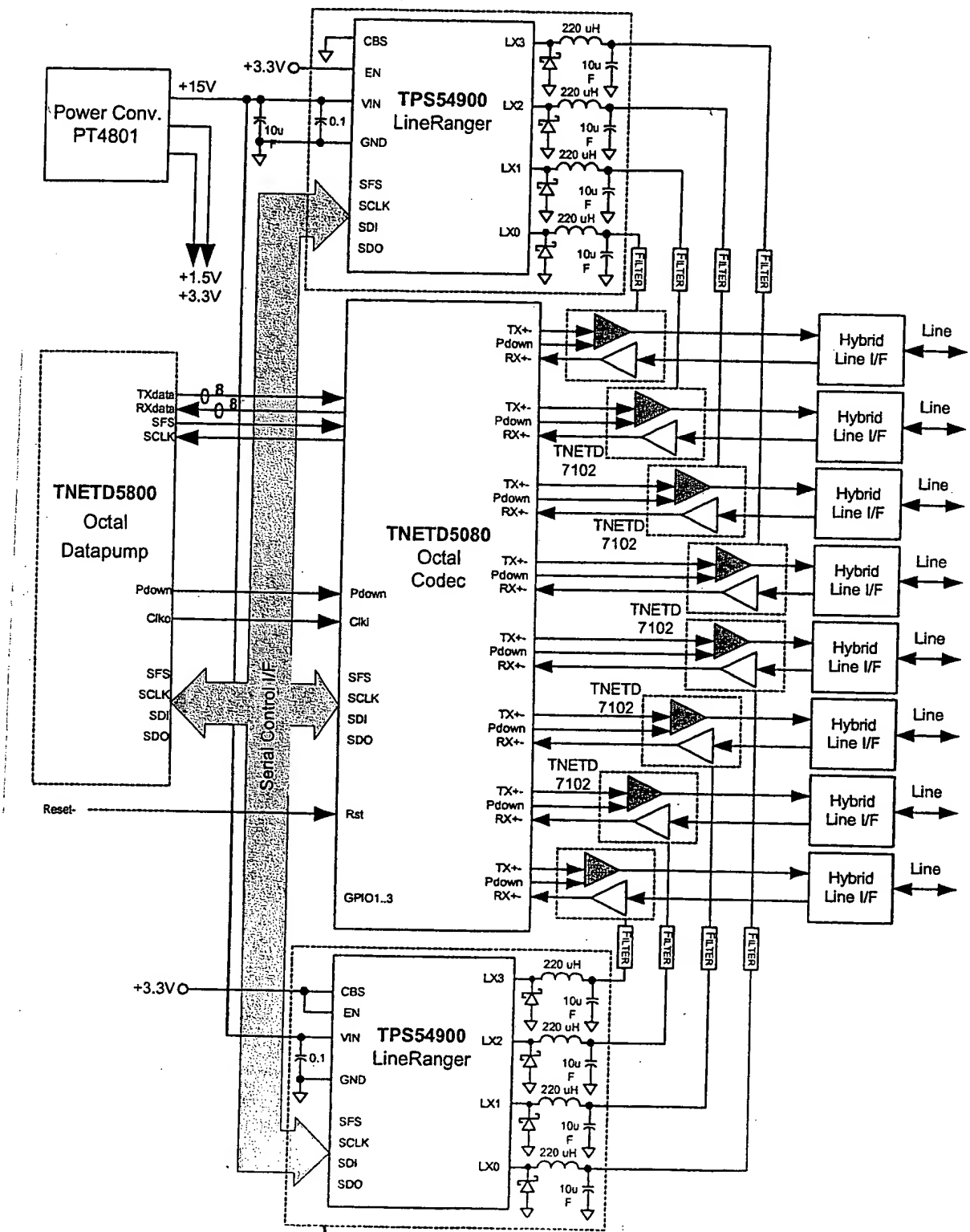


FIGURE 4

block diagram of eight channel AC5 line card with LineRanger option



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FIGURE 5